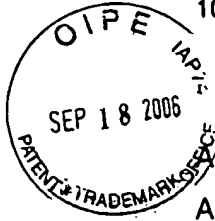


X-1266 US
10/701,835

PATENT
Conf. No. 7043



IN THE UNITED STATES PATENT OFFICE

Applicant: Tim Tuan
Assignee: Xilinx, Inc.
Title: "Structure and Method for Suppressing Sub-Threshold Leakage in Integrated Circuits"
Serial No.: 10/701,835 File Date: 11/04/2003
Examiner: David Lam Art Unit: 2827
Docket No.: X-1266 US Conf. No.: 7043

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

In response to the Final Office Action mailed August 25, 2006, Applicant submits the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

09/19/2006 MGE BREM1 00000006 240040 10701835

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